

FIG. 2 (PRIOR ART)

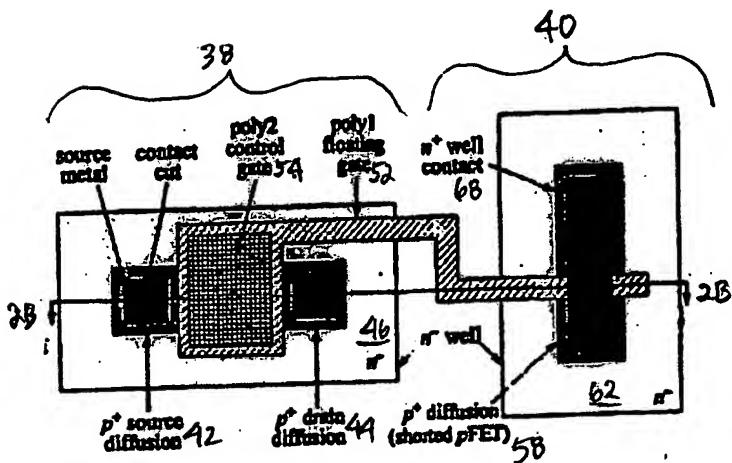


FIG. 3A

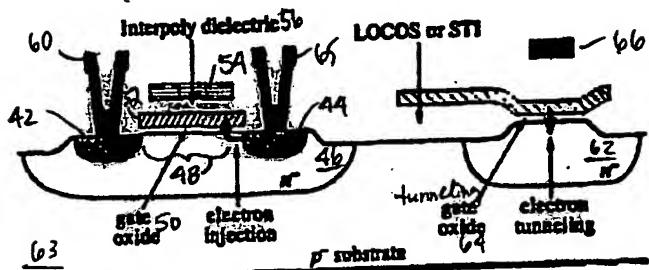


FIG. 3B

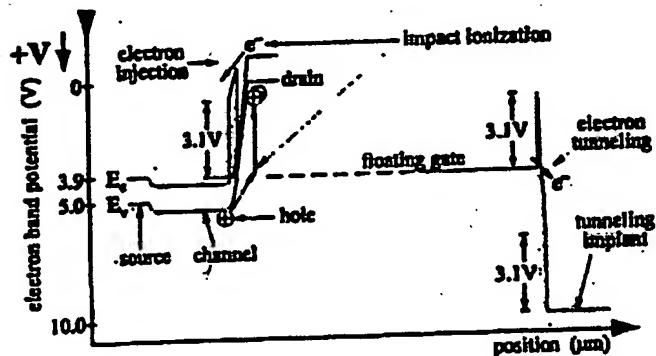


FIG. 3C

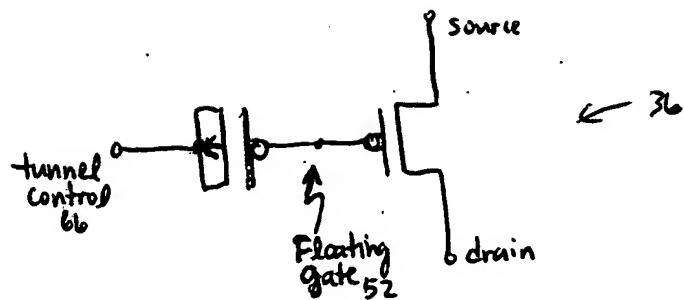


FIG. 3D

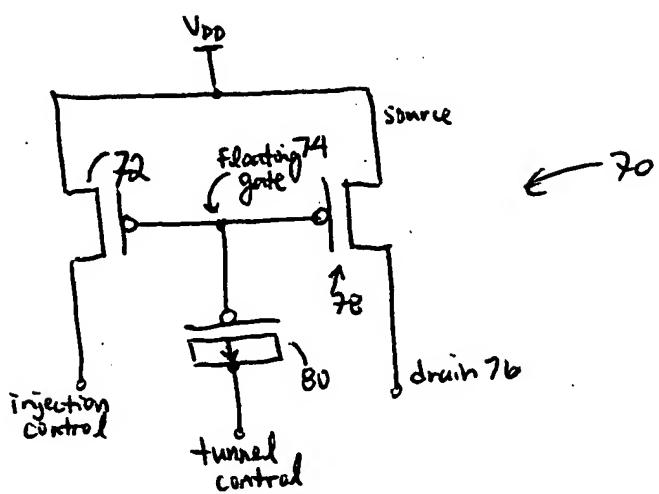


FIG. 3E

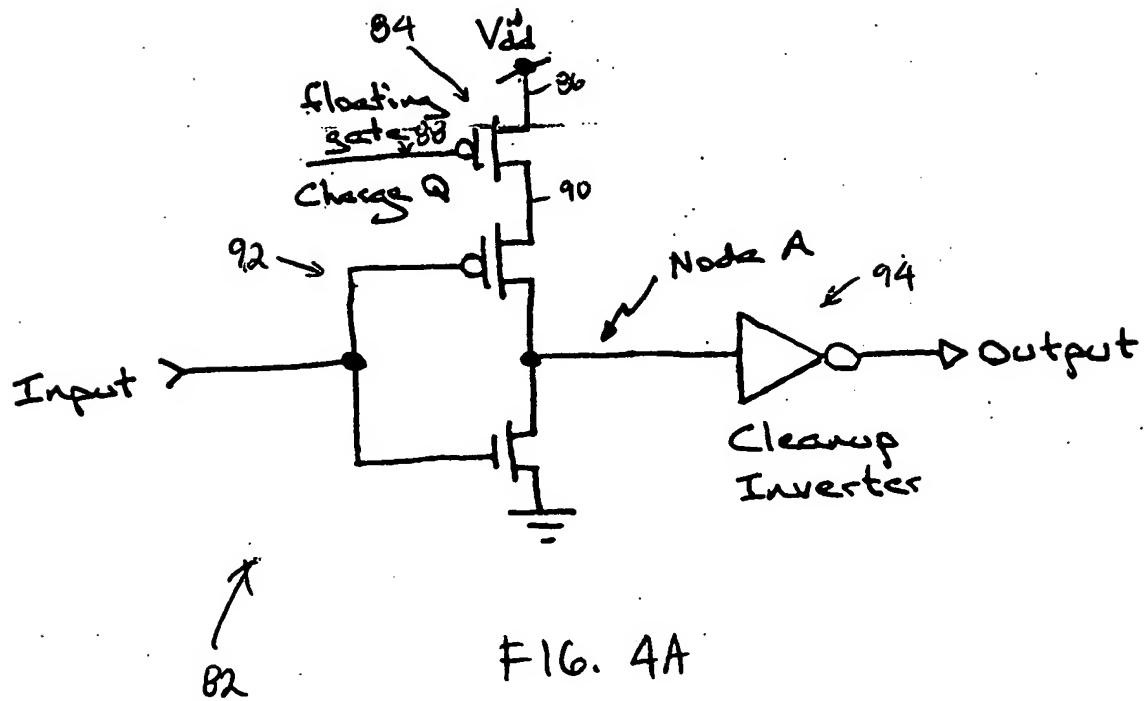


FIG. 4A

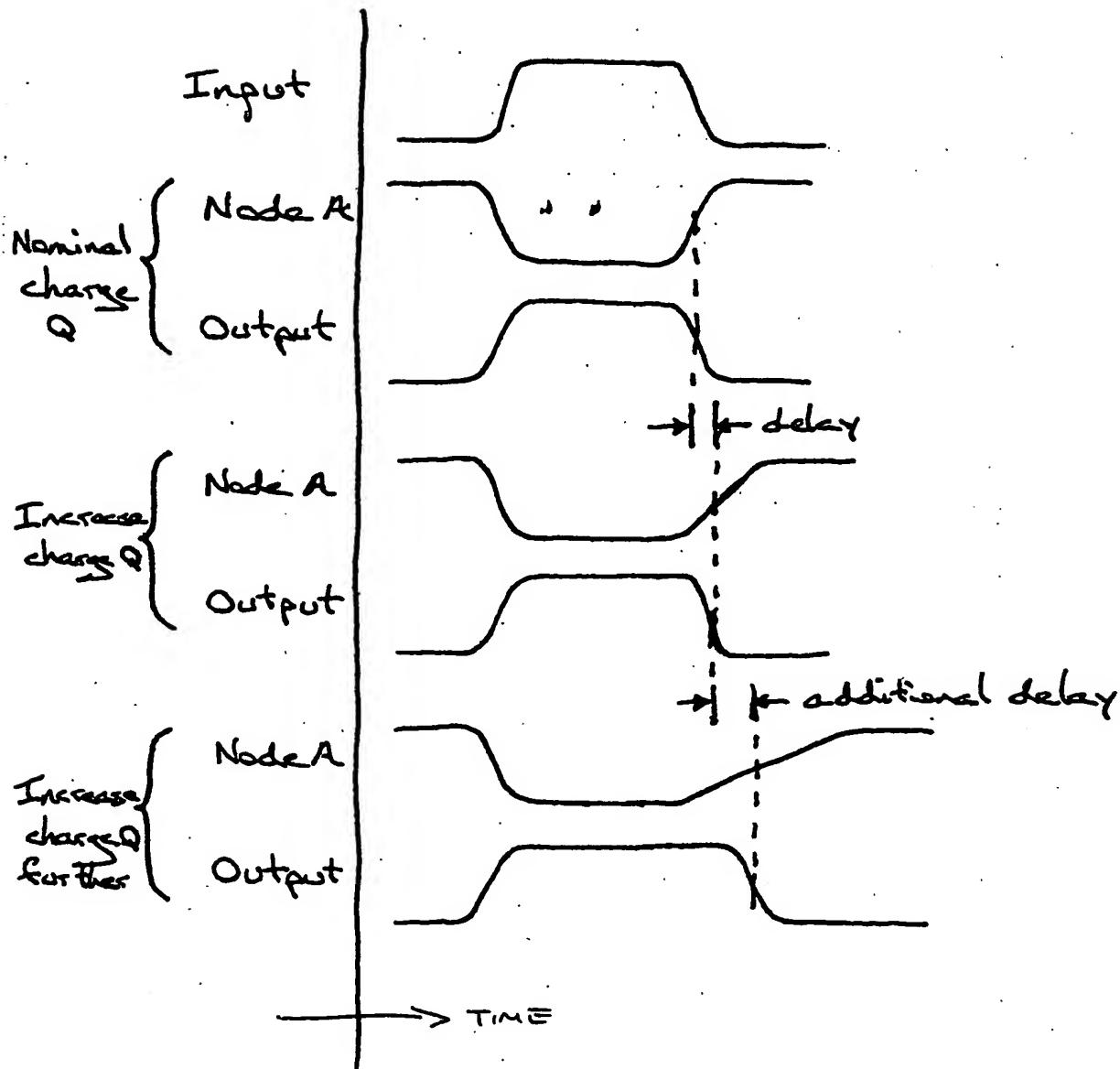


FIG. 4B

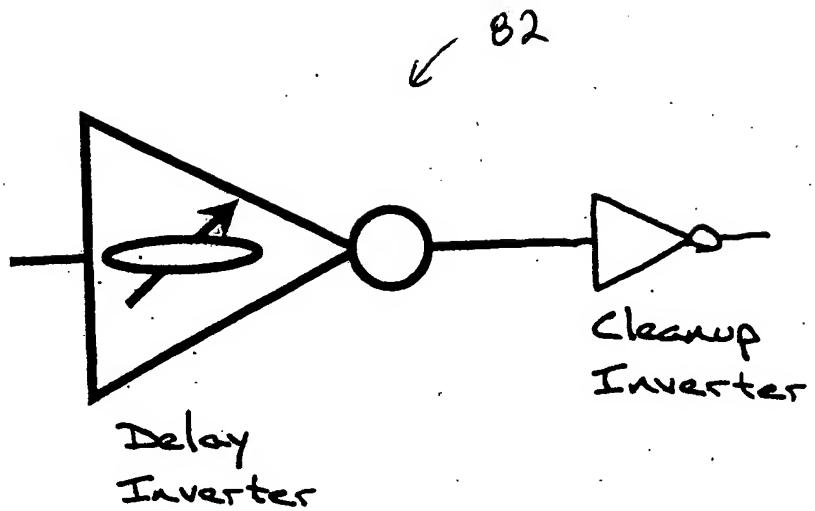


FIG. 4C

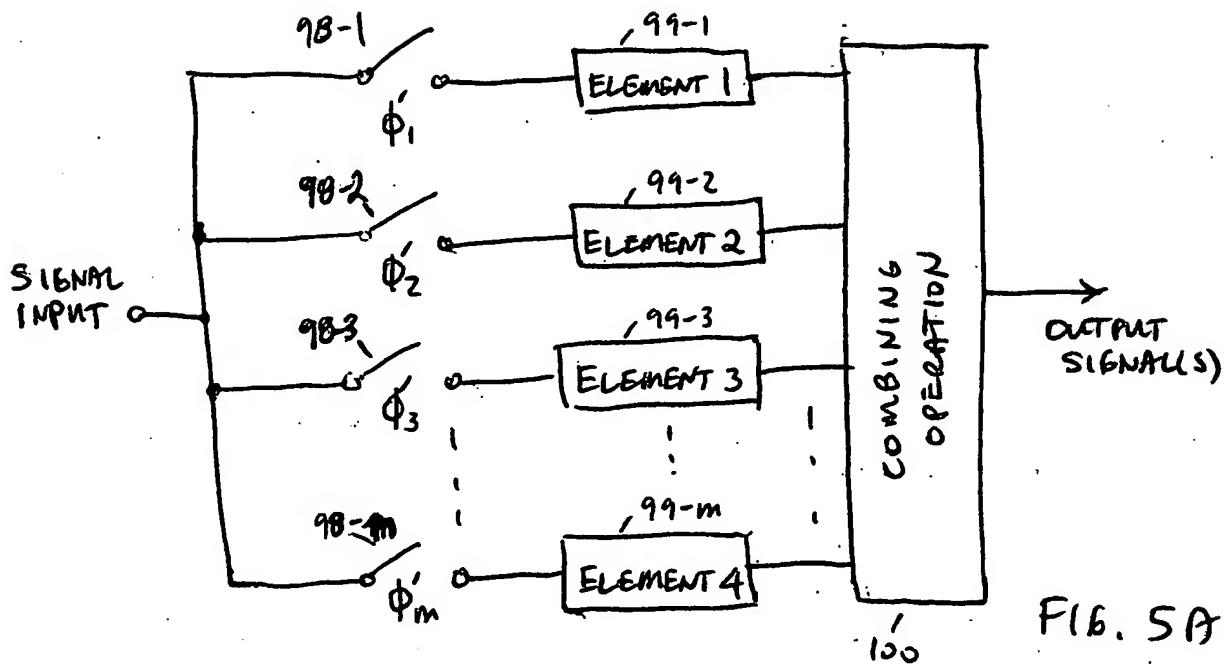


FIG. 5A

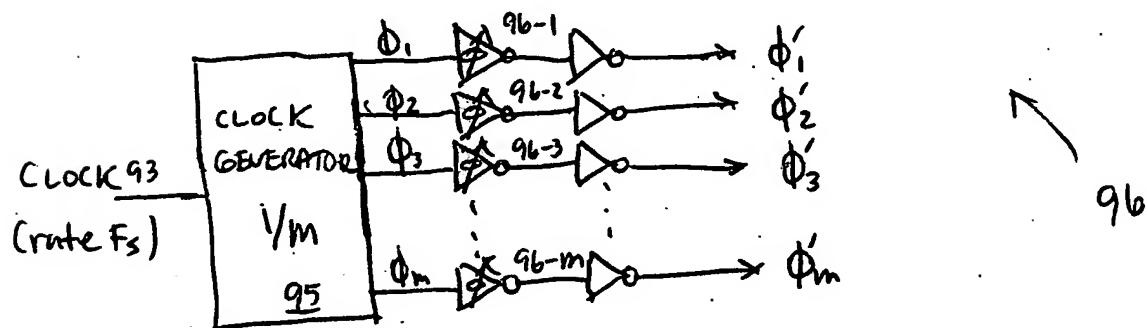


FIG. 5B

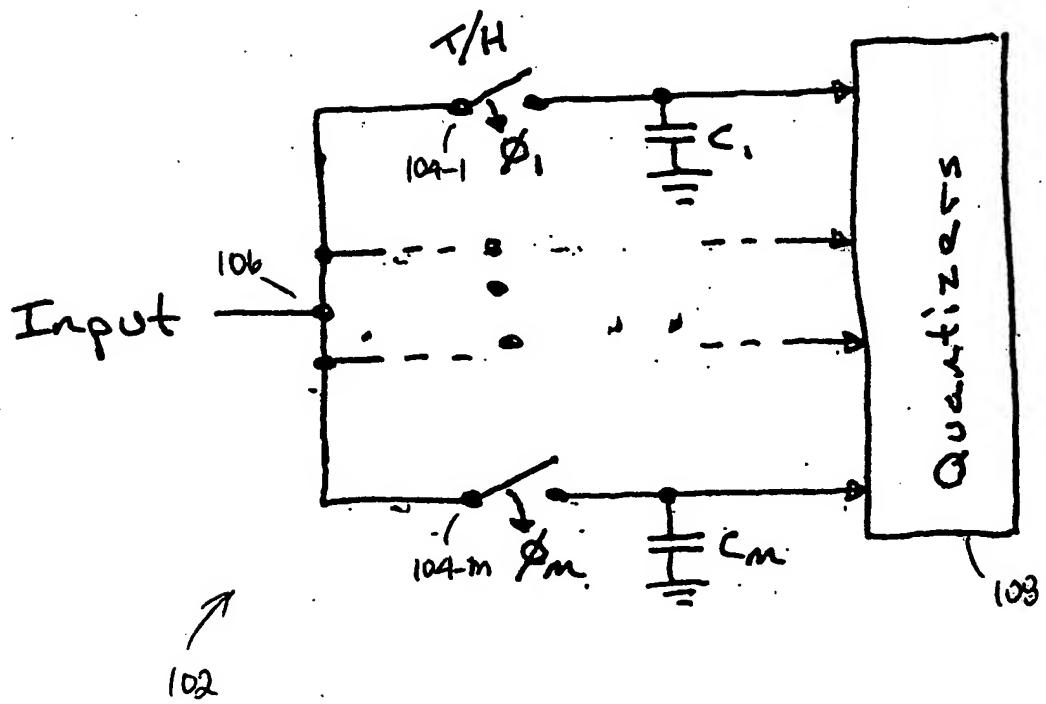


FIG. 6A

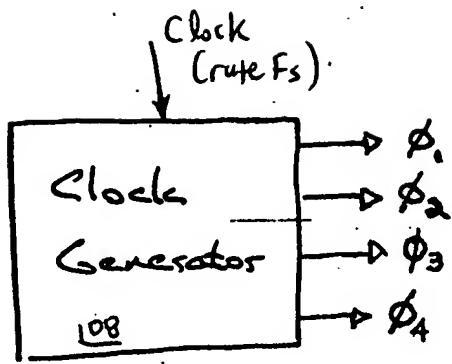


FIG. 6B

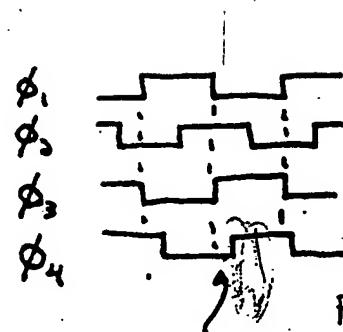
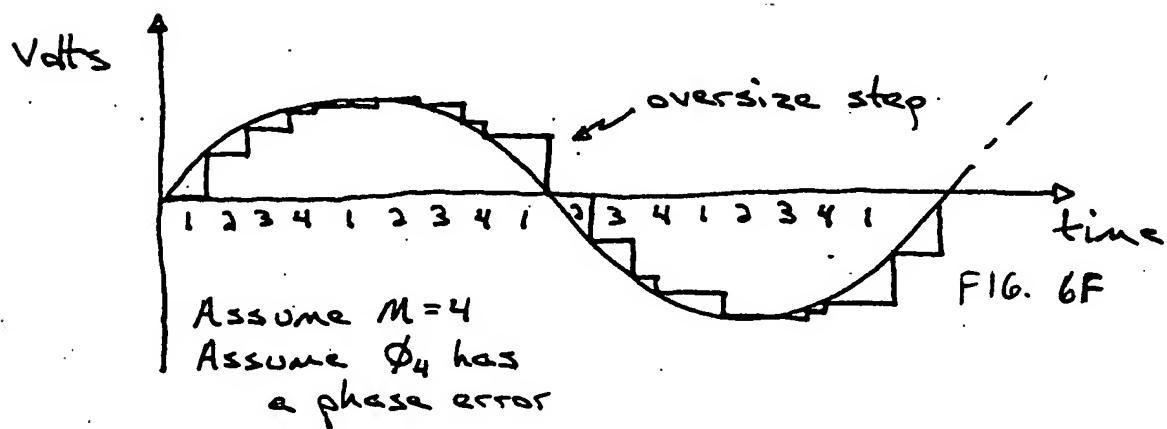
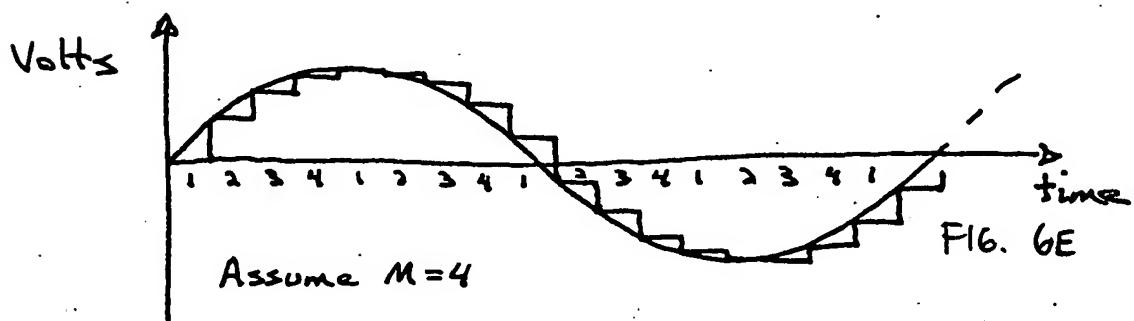
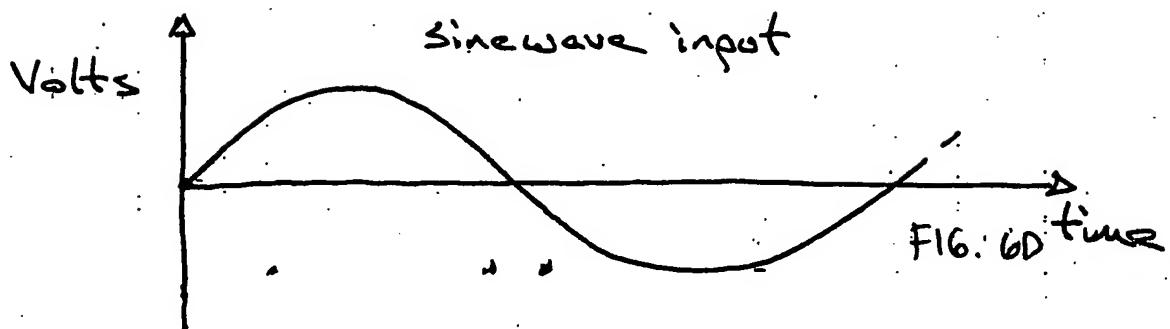


FIG. 6C

phase error
in ϕ_4



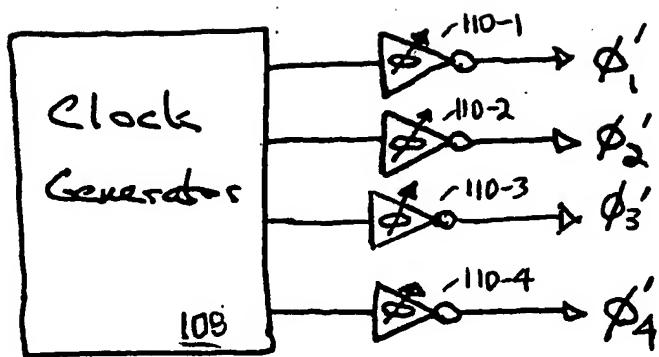


FIG. 7A

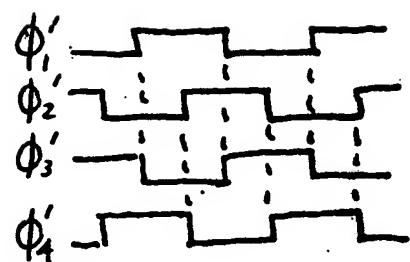


FIG. 7B

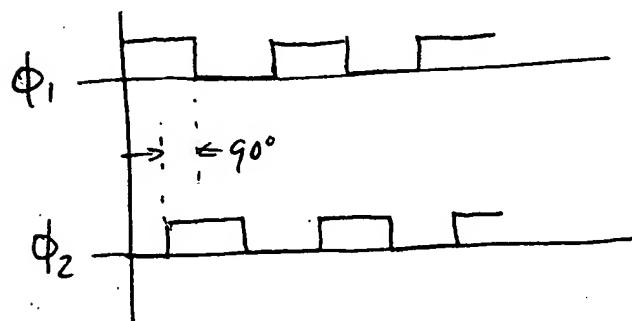
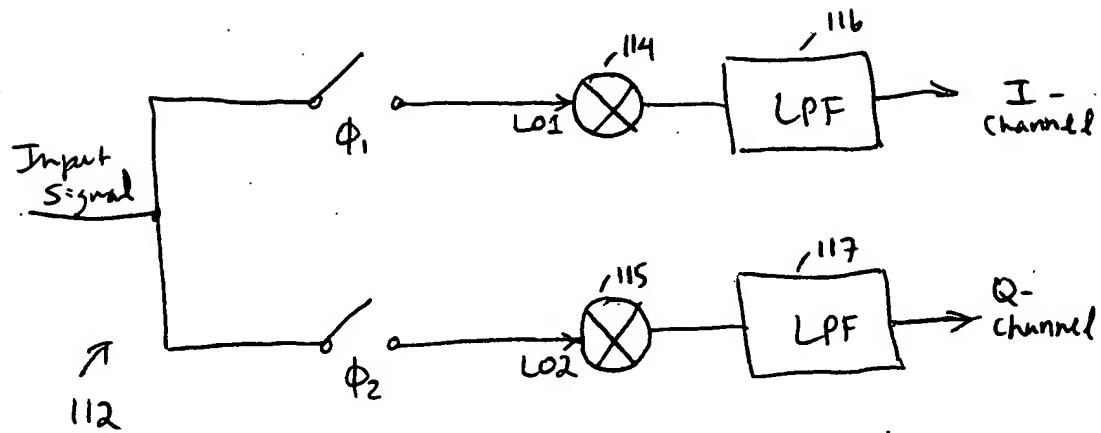


FIGURE 8A

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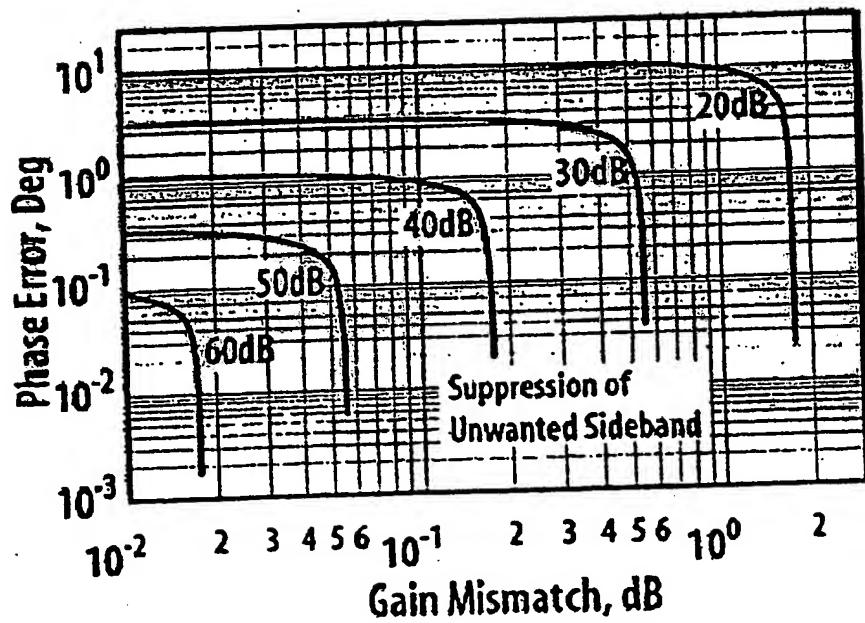


FIG. 8B

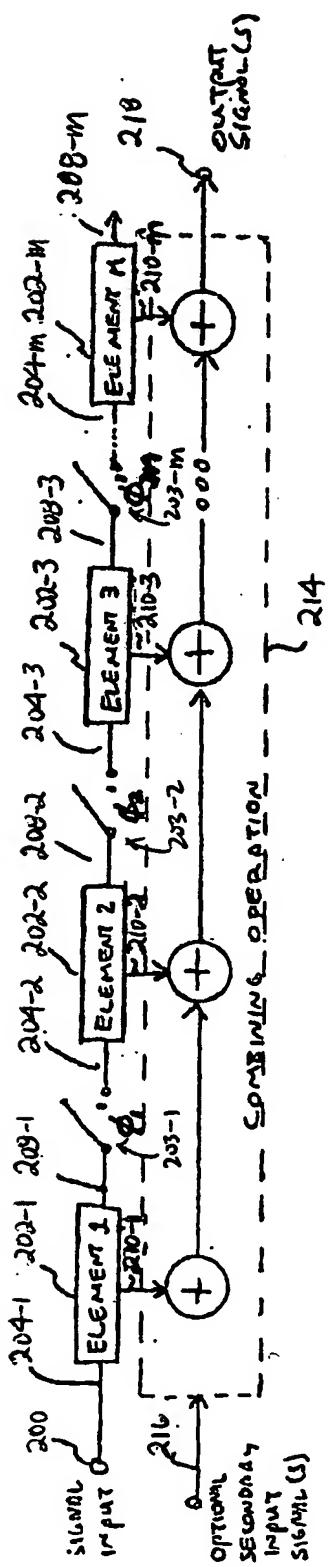


FIG. 9A

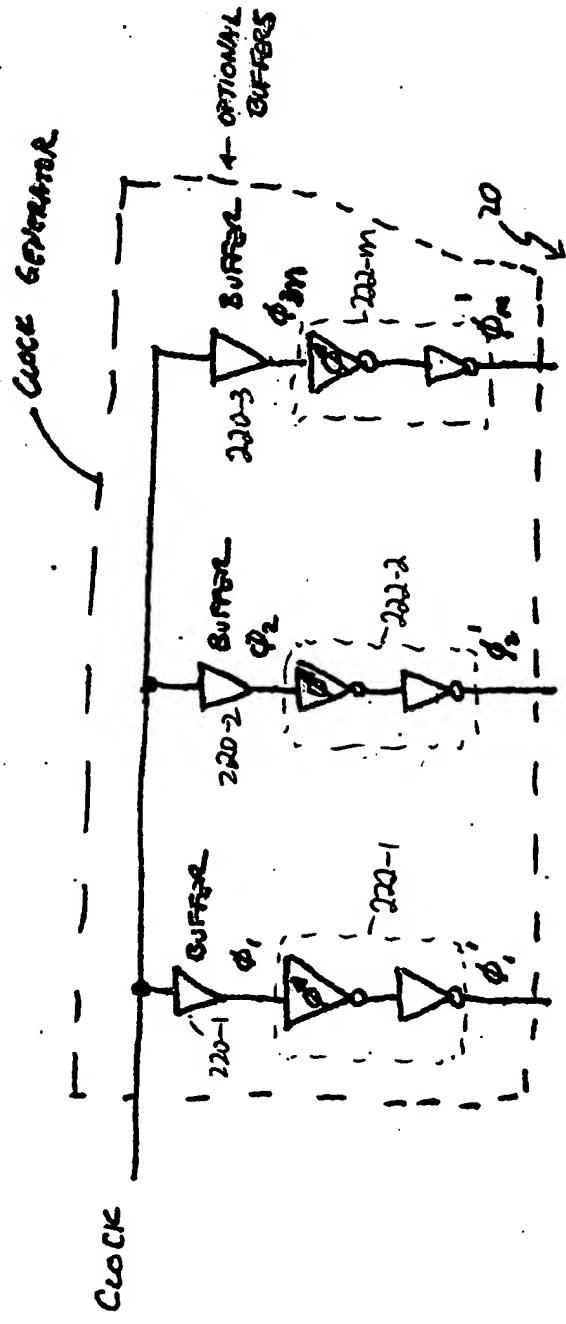


FIG. 9B

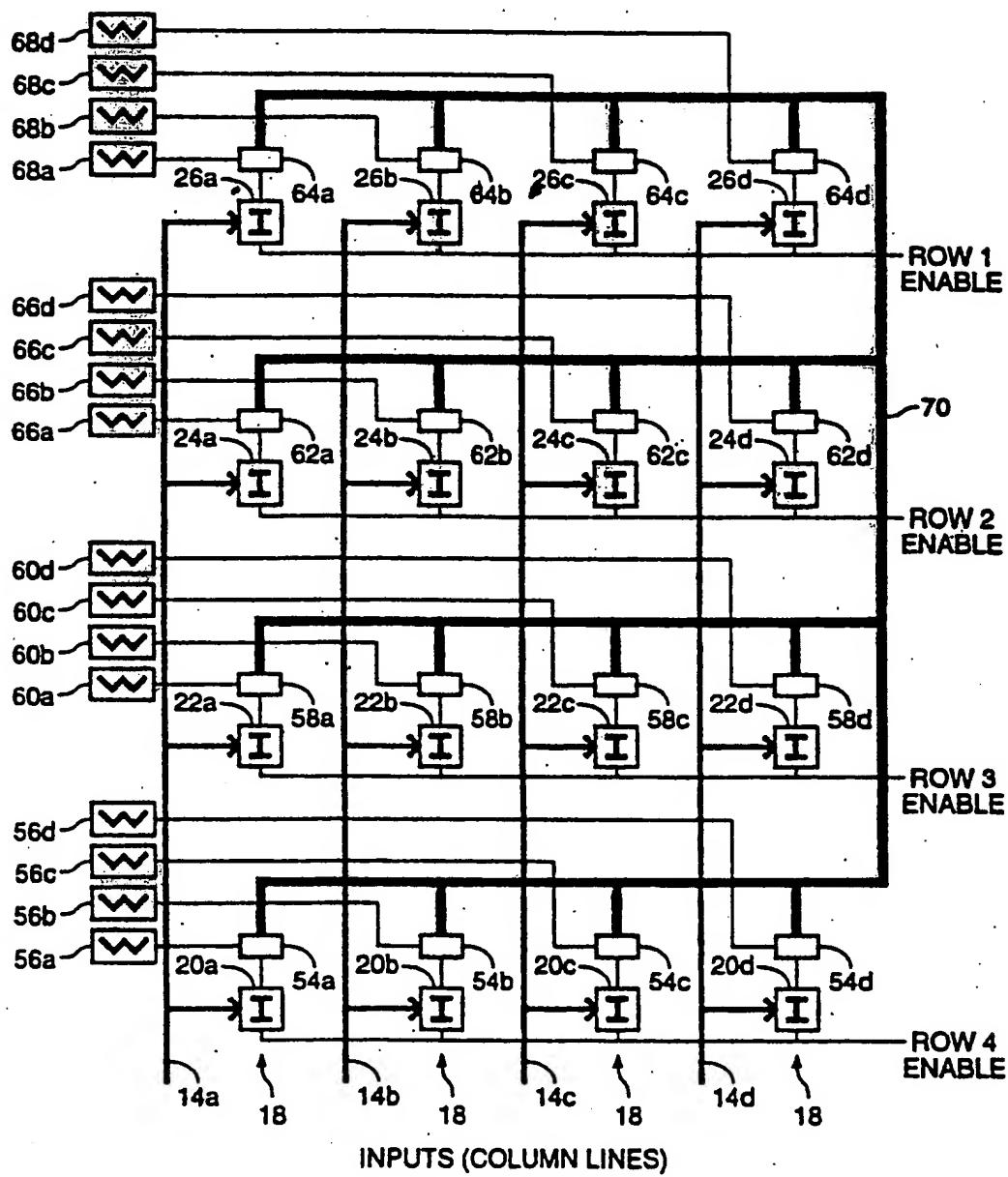


FIG. 10A
(PRIOR ART)

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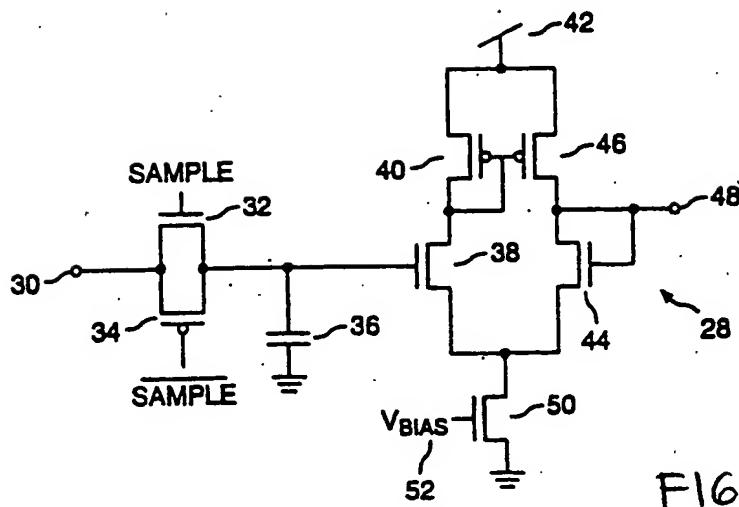
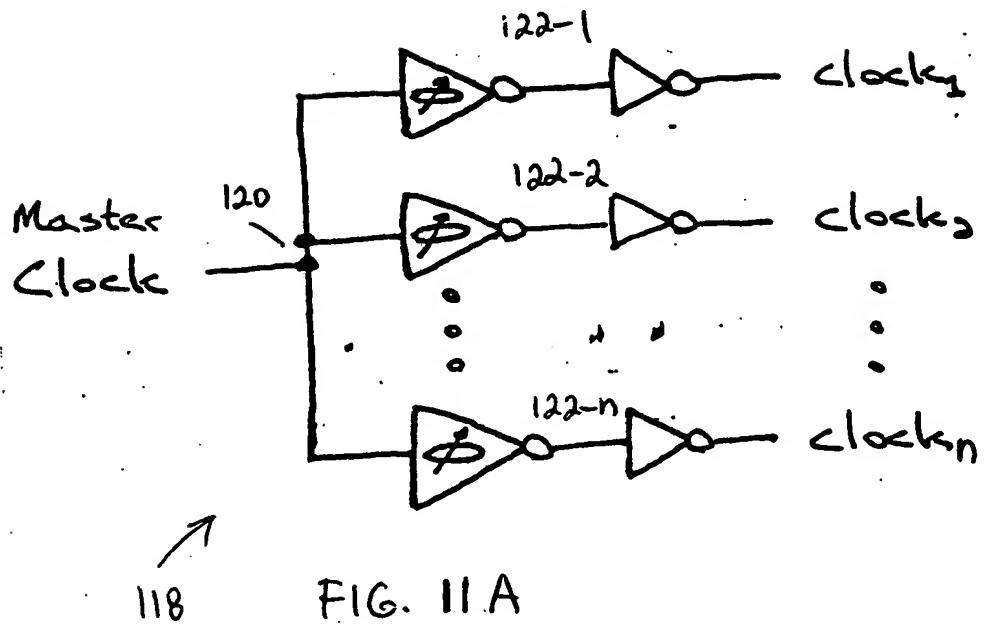


FIG. 10B
(PRIOR ART)



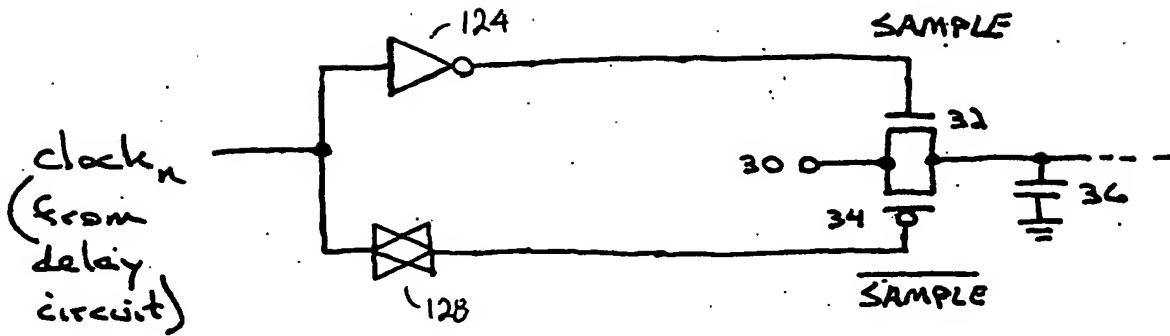


FIG. 11B

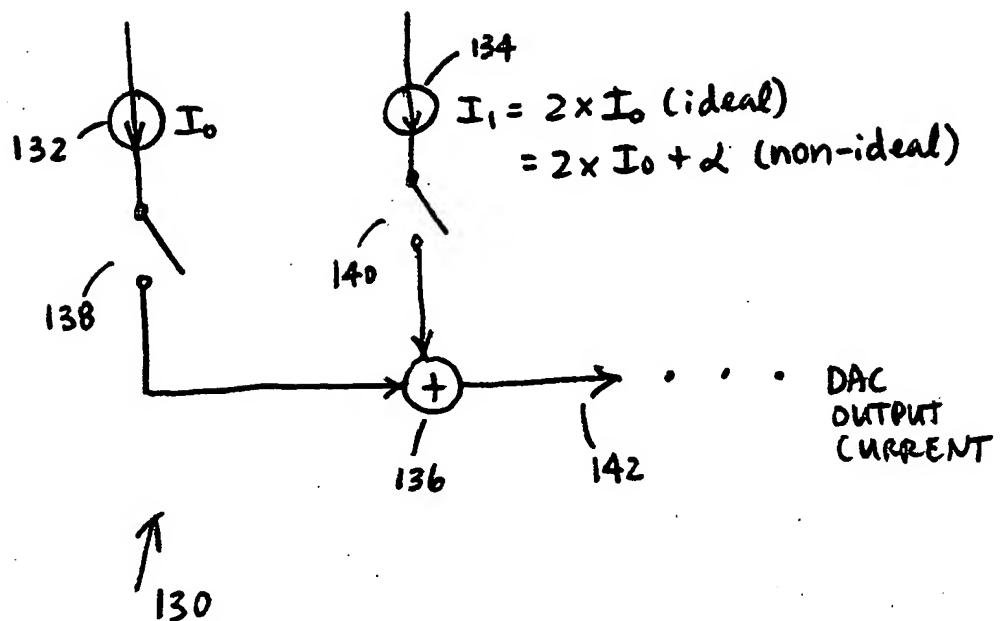
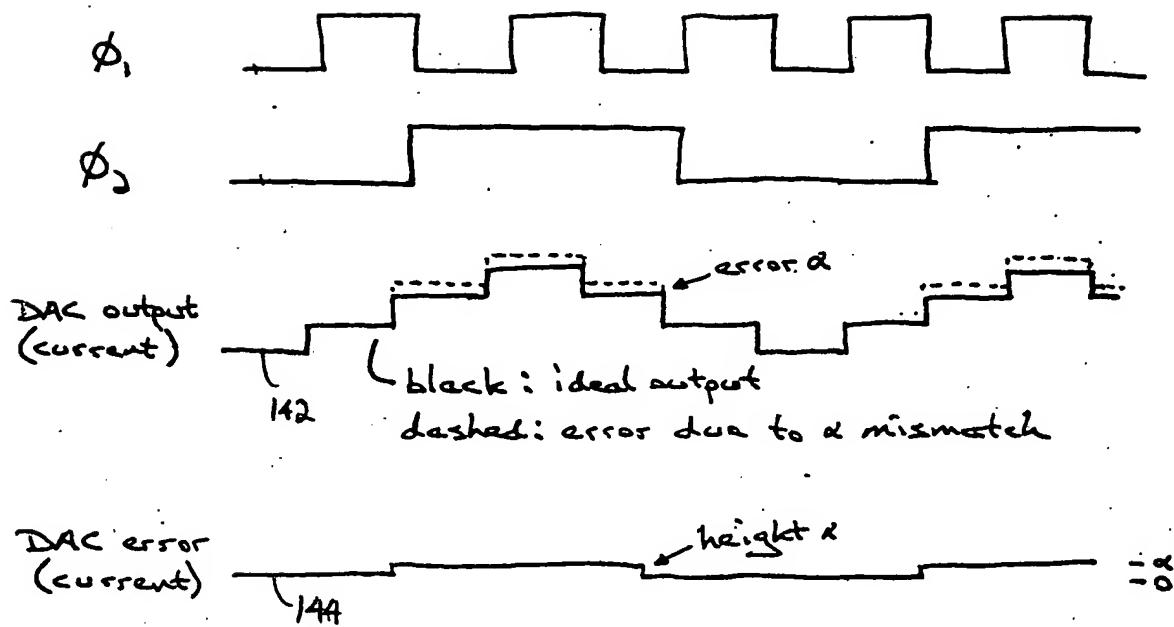


FIG. 12A

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F16. 12B

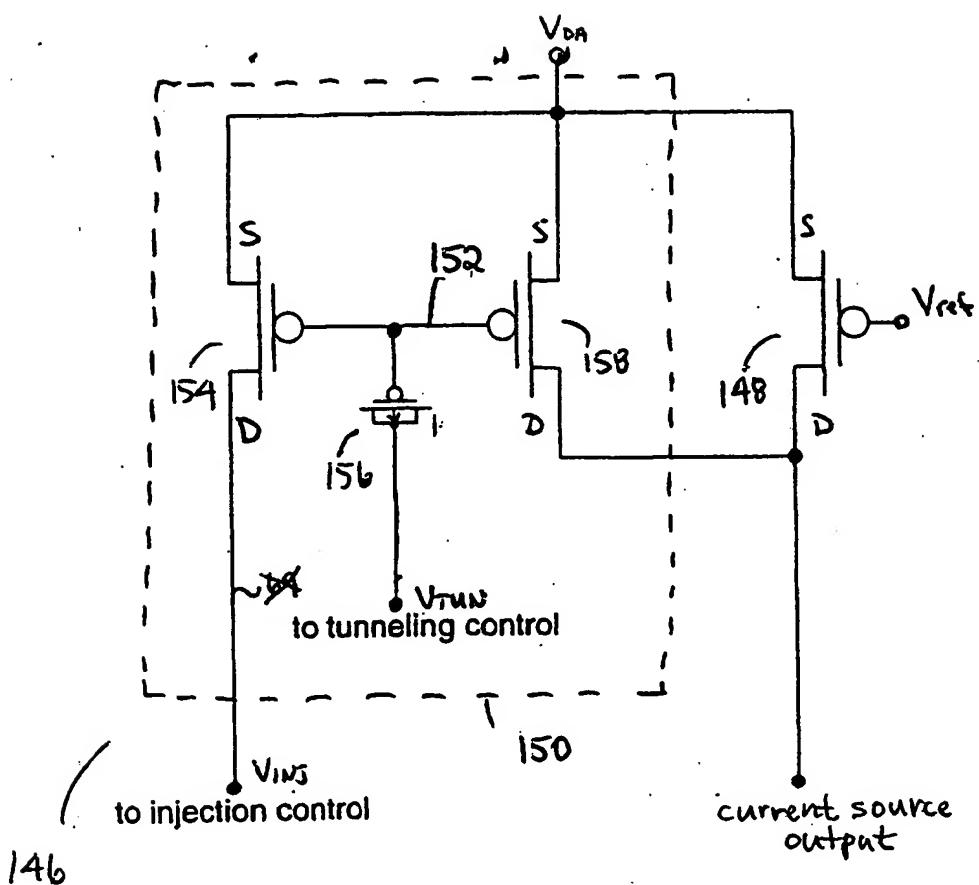


Fig. 13

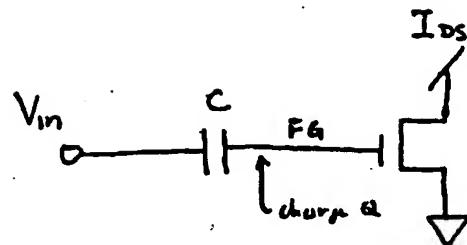


FIG. 14A

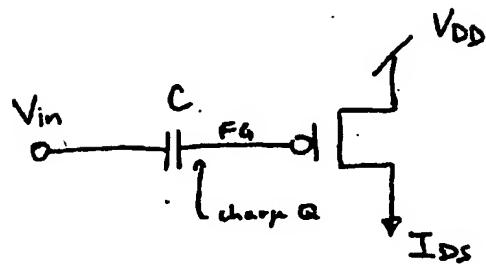


FIG. 14B

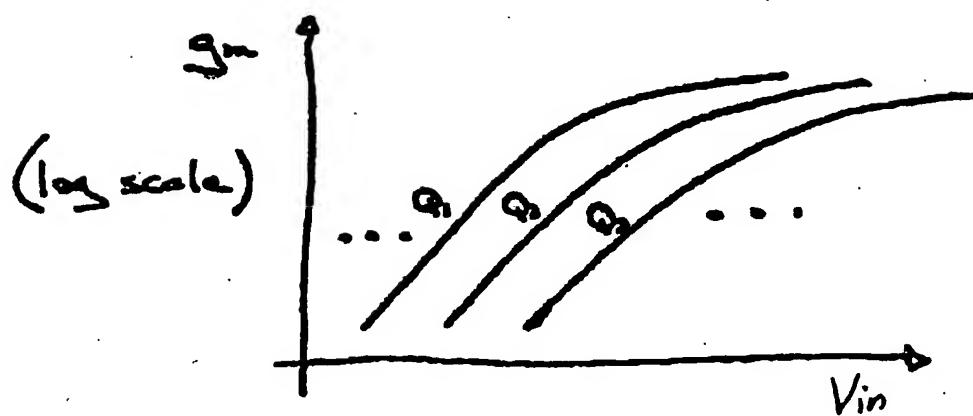


FIG. 14C

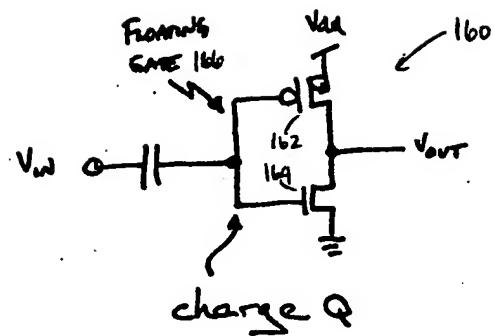


FIG. 15A

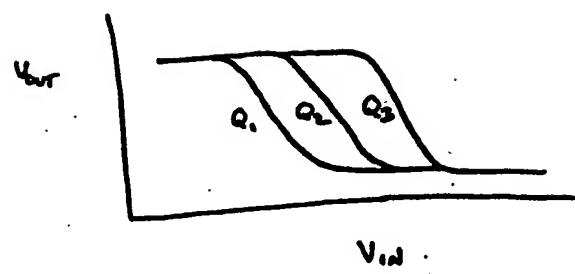


FIG. 15B

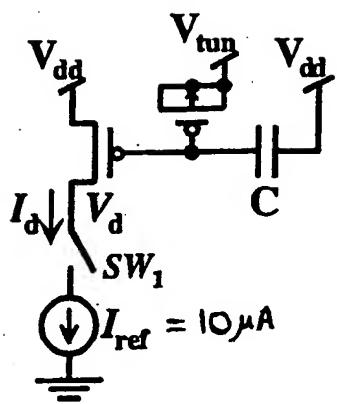


FIG. 16A

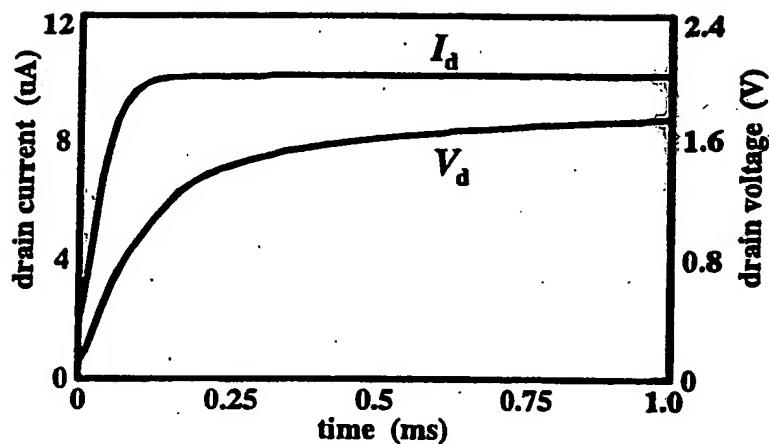


FIG. 16B

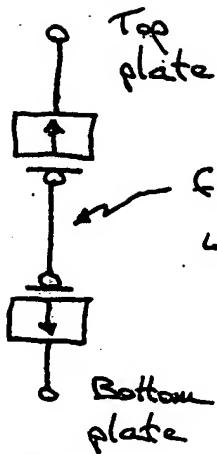


FIG. 17A

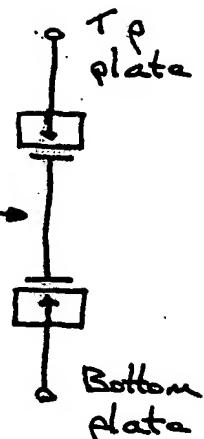


FIG. 17B

equivalent
circuit model \Rightarrow

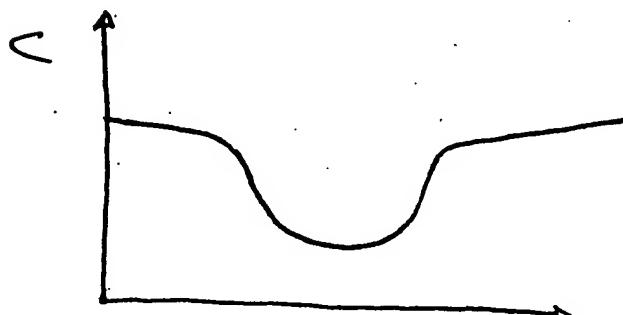
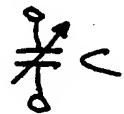
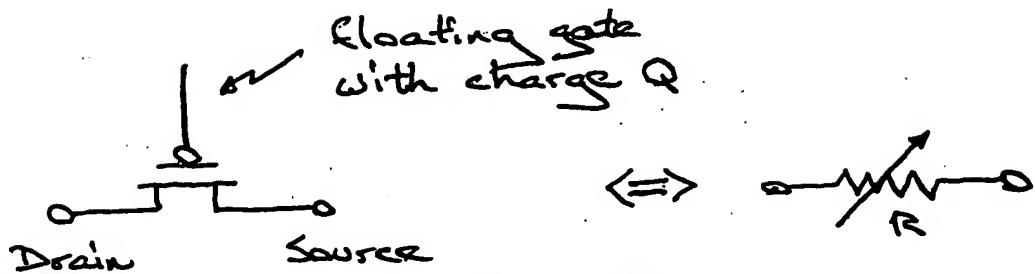
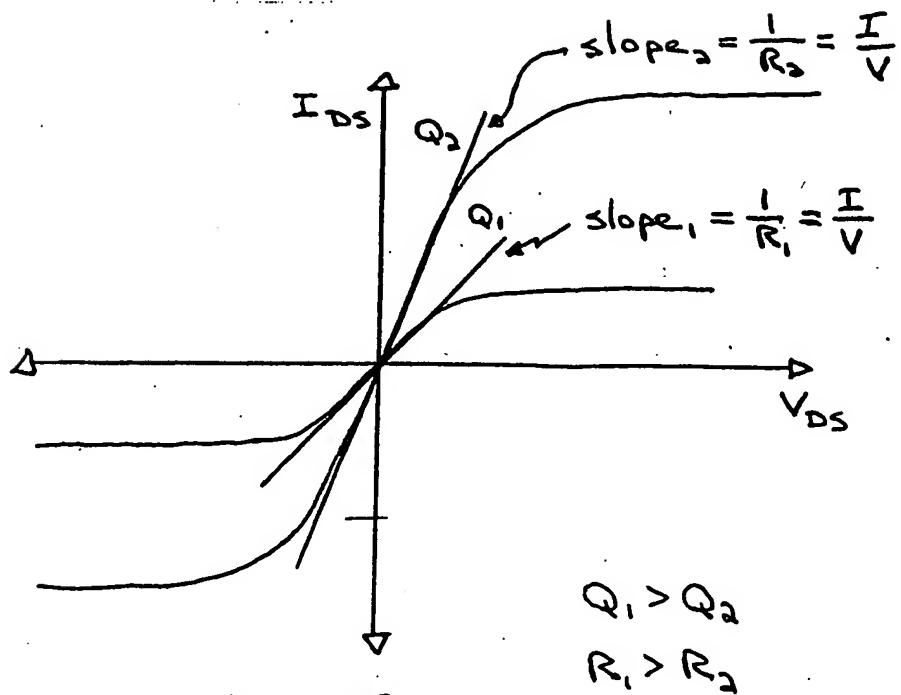


FIG. 17C



F16. 18A



F16. 18B

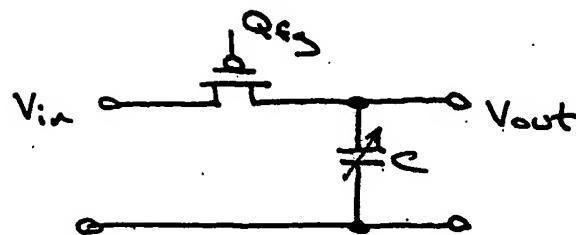


FIG. 19A

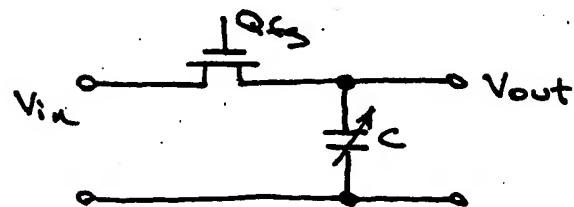


FIG. 19B

$$\zeta_n = RC \quad (n=1, 2, \dots)$$

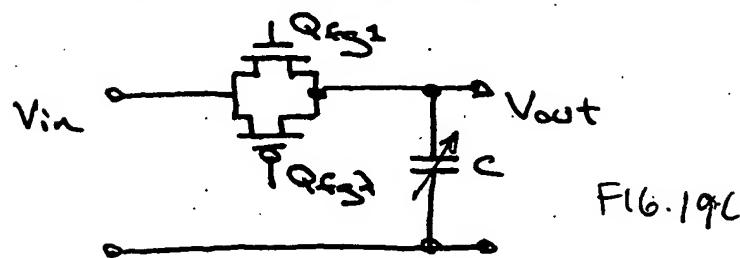


FIG. 19C

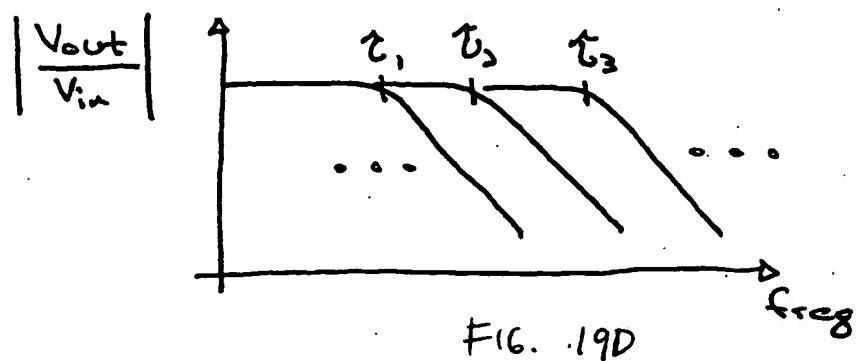


FIG. 19D

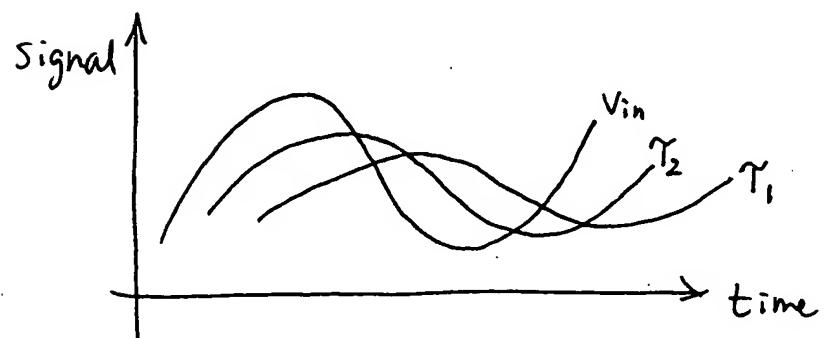


FIG. 19E

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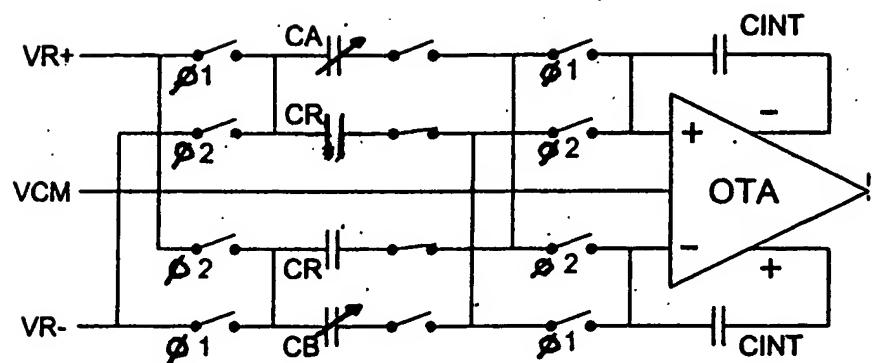


FIG. 20